

Electronic Acknowledgement Receipt

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Application Number:	10808341
International Application Number:	
Confirmation Number:	5441
Title of Invention:	DSP (digital signal processing) architecture with a wide memory bandwidth and a memory mapping method thereof
First Named Inventor/Applicant Name:	Hyun-kyu Yun
Customer Number:	38209
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Amendment After Final	Amendment_after_Final.pdf	997716	no	19
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8341	no	2
Warnings:					
Information:					
Total Files Size (in bytes):				1006057	
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